## 1.1. S34ML-1 Product Families

## 41 nm SLC NAND

41 nm SLC NAND were introduced in Jun 2012 and utilize tunnel Oxide, Polysilicon floating gate and interconnections are three metal layers with contact plugs and barrier metals. The 1st Metal layer for 41 nm SLC NAND is using Copper.

# Data Summary and Failure Rate Estimation using Exponential Model HTOL Stress Temperature - 125°C

|                                   |                     | int / Test<br>sult     |          | Modeling Parameters @ 55°C |     |     |               |                     | Average Failure Rate    |  |
|-----------------------------------|---------------------|------------------------|----------|----------------------------|-----|-----|---------------|---------------------|-------------------------|--|
| Failure Mechanisms                | Early Life<br>(hrs) | Inherent<br>Life (hrs) | Ea<br>eV | TAF                        | VAF | OAF | MTTF<br>(yrs) | Early Life<br>(PPM) | Inherent Life<br>(FITS) |  |
|                                   | 96                  | 1000                   |          |                            |     |     | (313)         | (1.1.11)            | (1110)                  |  |
| Sample Size                       | 500                 | 150                    |          |                            |     |     |               |                     |                         |  |
| 125C, Zero fails, Process ave. Ea | 0                   | 0                      | 0.7      | 74                         | 1   | 74  |               | 0                   | 12                      |  |
|                                   |                     |                        |          |                            |     |     | 9259          |                     |                         |  |

## Data Retention Bake - 150°C

| Reliability Stress | Sample Size | Reject | PPM | FITS |
|--------------------|-------------|--------|-----|------|
| 1000               | 77          | 0      | 0   | <1   |

## Endurance - 90°C

| Reliability Stress | Sample Size | Reject | PPM | FITS |
|--------------------|-------------|--------|-----|------|
| 10000              | 60          | 0      | 0   | 2    |
| 100000(Decade)     | 64          | 0      | 0   | 2    |

#### 1.2. S34ML-2 Product

#### **Families**

#### 32 nm SLC NAND

32 nm SLC NAND were introduced in October 2012 and utilize tunnel Oxide, Polysilicon floating gate and interconnections are three metal layers with contact plugs and barrier metals. The 1st Metal layer for 32 nm SLC NAND is using Copper

# Data Summary and Failure Rate Estimation using Exponential Model HTOL Stress Temperature - 125°C

|                                   |                     | oint / Test<br>sult    |          | Modeling Parameters @ 55°C |     |     |               | Average Failure Rate |                         |
|-----------------------------------|---------------------|------------------------|----------|----------------------------|-----|-----|---------------|----------------------|-------------------------|
| Failure Mechanisms                | Early Life<br>(hrs) | Inherent<br>Life (hrs) | Ea<br>eV | TAF                        | VAF | OAF | MTTF<br>(yrs) | Early Life<br>(PPM)  | Inherent Life<br>(FITS) |
|                                   | 96                  | 1000                   | •        |                            |     |     | (3.5)         | ()                   | (1110)                  |
| Sample Size                       | 500                 | 150                    |          |                            |     |     |               |                      |                         |
| 125C, Zero fails, Process ave. Ea | 0                   | 0                      | 0.7      | 74                         | 1   | 74  |               | 0                    | 9                       |
| SKVH                              |                     | h                      |          |                            |     |     |               |                      |                         |
|                                   |                     |                        |          |                            |     |     | 12198         |                      |                         |

#### Data Retention Bake - 150°C

| Reliability Stress | Sample Size | Reject | PPM | FITS |
|--------------------|-------------|--------|-----|------|
| 1000               | 77          | 0      | 0   | <1   |

#### Endurance - 90°C

| Reliability Stress | Sample Size | Reject | PPM | FITS |
|--------------------|-------------|--------|-----|------|
| 10000              | 60          | 0      | 0   | 0    |
| 100000(Decade)     | 64          | 0      | 0   | 2    |

# 2. Data Summaries by Package Family

## 2.1. BGA (Ball Grid Array)

| Reliability Stress |           | Sample<br>Size | Reject | Failure Rate<br>PPM |
|--------------------|-----------|----------------|--------|---------------------|
| HAST               | 96hrs     | 1510           | 0      | 0                   |
|                    | 264hrs    | 3542           | 0      | 0                   |
| HIGH TEMP STORAGE  | 1000hrs   | 5080           | 0      | 0                   |
| TEMP CYCLE         | 500cycle  | 3875           | 0      | 0                   |
|                    | 1000cycle | 1728           | 0      | 0                   |
| UNBIASED HAST TEST | 96hrs     | 3322           | 0      | 0                   |
|                    | 264hrs    | 1761           | 0      | 0                   |

# 2.2. TSOP (Thin Small Outline Package)

| Reliability Stress   |          | Sample<br>Size | Reject | Failure Rate<br>PPM |
|----------------------|----------|----------------|--------|---------------------|
| HAST                 | 96hrs    | 4174           | 0      | 0                   |
|                      | 264hrs   | 13<br>0        | 0      | 0                   |
| HIGH TEMP STORAGE    | 1000hrs  | 4900           | 0      | 0                   |
| PRESSURE COOKER TEST | 96hrs    | 1035           | 0      | 0                   |
|                      | 168hrs   | 1863           | 0      | 0                   |
| TEMP CYCLE           | 500cycle | 5625           | 0      | 0                   |
| UNBIASED HAST TEST   | 96hrs    | 1863           | 0      | 0                   |

